

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	0	@ad<"19991217" and (phase adj detector) and (time adj stamp adj generator) and 327/163.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/31 13:58
L4	0	@ad<"19991217" and (phase adj detector) and (time adj stamp adj generator) and 327/160.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/31 13:58
L3	0	@ad<"19991217" and (phase adj detector) and (time adj stamp adj generator) and (ATM or (cell adj relay)) and 327/157.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/31 13:58
L2	0	@ad<"19991217" and (phase adj detector) and (time adj stamp adj generator) and (ATM or (cell adj relay)) and 327/144-145.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/31 13:57
L1	0	@ad<"19991217" and (phase adj detector) and (time adj stamp adj generator) and (ATM or (cell adj relay)) and 375/144-145.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/31 13:57
S33	10	@ad<="19991217" and (phase adj detector) and (SRTS or RTS) and (ATM or (cell adj relay))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/31 13:56
S48	0	@ad<="19991217" and ATM and AAL1 and (adaptive same clock\$3) and 327/2.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/28 14:17
S47	0	@ad<="19991217" and ATM and AAL1 and (adaptive same clock\$3) and 327/156.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/28 14:17
S46	0	@ad<="19991217" and ATM and AAL1 and (adaptive same clock\$3) and 348/536.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/28 14:17
S45	8	@ad<="19991217" and ATM and AAL1 and (adaptive same clock\$3) and 375/371-376.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/28 14:17

S44	93	@ad<"19991217" and (adaptive same clock\$3) and 375/371-376.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/28 14:16
S43	0	@ad<="19991217" and ATM and AAL1 and (adaptive same clock\$3) and 375/371-376	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/28 14:16
S5	20	@ad<="19991217" and ATM and AAL1 and (adaptive same clock\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/01/28 14:15
S42	22	@ad<="19991217" and (SRTS or RTS) and (ATM or (cell adj relay)) and (fill adj level)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 10:46
S32	0	@ad<="19991217" and (phase adj detector).ti. and (SRTS or RTS) and (ATM or (cell adj relay))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 10:45
S24	24	@ad<="19991217" and (PLL or DLL) and (jitter with filter with divider)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 10:44
S41	4	@ad<="19991217" and (summer or adder).ti. and (register with output with adder) and PLL	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 10:05
S40	961	@ad<="19991217" and (summer or adder).ti. and (register with output with adder)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 10:04
S39	2481	@ad<="19991217" and (summer or adder).ti. and register	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 10:04
S38	137	@ad<="19991217" and summer.ti. and register	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 10:04

S37	3	@ad<="19991217" and (counter with (error adj flag) with disable)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 10:03
S35	16	@ad<="19991217" and counter.ti. and (error adj flag)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 09:48
S36	0	@ad<="19991217" and counter.ti. and ((error adj flag) same disable)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 09:45
S34	0	@ad<="19991217" and ((up or down) adj counter).ti. and (error adj flag)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 09:44
S1	0	@ad<="19991217" and (ATM adj AAL1 adj clock adj recovery adj PLL)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/06 09:43
S21	409	@ad<="19991217" and (phase adj detector).ti. and (PLL or DLL)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 17:33
S31	14	@ad<="19991217" and PLL and (intrinsic adj jitter)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 16:50
S30	34	@ad<="19991217" and PLL and (jitter adj (filter or reduction)) and divider	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 16:49
S29	77	@ad<="19991217" and PLL and (jitter adj (filter or reduction))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 16:20
S28	10	@ad<="19991217" and PLL and oscillator and (jitter adj filter)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 16:15

S27	0	@ad<="19991217" and PLL and oscillator and (jitter adj redut\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 16:05
S9	0	@ad<="19991217" and PLL and filter and (phase adj detector) and oscillator and (jitter adj redut\$3 adj circuit) and divider	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 16:04
S26	10	@ad<="19991217" and peres-mauricio.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 14:56
S25	2	("5602884").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2004/02/05 14:55
S23	977	@ad<="19991217" and (PLL or DLL) and (jitter same filter)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 13:30
S8	5	@ad<="19991217" and PLL and filter and (phase adj detector) and oscillator and (jitter adj circuit) and divider	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 13:28
S22	2	@ad<="19991217" and (phase adj detector).ti. and (PLL or DLL) and counter and subtractor and comparator	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 13:19
S20	4	@ad<="19991217" and ((PLL or DLL) same (valid adj input))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 13:18
S19	8	@ad<="19991217" and ((PLL or DLL) same handover)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 13:12
S18	0	@ad<="19991217" and ((PLL or DLL) adj handover)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 13:10

S17	6	@ad<="19991217" and ((PLL or DLL) with handover)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 13:10
S15	6	@ad<="19991217" and (PLL with handover)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/05 13:10
S16	1	@ad<="19991217" and PLL.ti. and handover	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/04 18:43
S14	0	@ad<="19991217" and (PLL same (handover adj mold))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/04 18:41
S13	1	@ad<="19991217" and (PLL same handover) and ATM	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/04 18:40
S12	39	@ad<="19991217" and PLL and handover	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/04 18:37
S11	17	@ad<="19991217" and PLL and filter and (phase adj detector) and (digital adj oscillator) and divider	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/04 18:36
S10	1743	@ad<="19991217" and PLL and filter and (phase adj detector) and oscillator and divider	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/04 18:35
S7	7	@ad<="19991217" and ATM and AAL1 and (adaptive same clock\$3) and PLL and fill	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/04 18:26
S6	11	@ad<="19991217" and ATM and AAL1 and (adaptive same clock\$3) and PLL	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/04 18:10

S4	0	@ad<="19991217" and ATM and phaseword and (clock adj recovery) and PLL	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/04 18:08
S3	10	@ad<="19991217" and ATM and AAL1 and (clock adj recovery) and PLL	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/04 18:06
S2	0	@ad<="19991217" and ATM and AAL1 and (clock adj recovery adj PLL)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/02/04 17:47